In the Claims:

1-18. (Canceled)

19. (Original) An antenna, comprising:

an RF input pin;

at least one antenna element connected to the RF input pin; and

at least one electronic component connected to the RF input pin, said electronic component being configured to identify at least one property of the antenna.

20. (Original) The antenna according to Claim 19, wherein said at least one electronic component is a resistor having a value related to said at least one property of the antenna.

21. (Original) The antenna according to Claim 19, wherein said at least one electronic component is a circuit having a resonant frequency related to said at least one property of the antenna.

22. (Original) The antenna according to Claim 19, wherein said at least one electronic component is a microchip configured to transmit a value related to antenna properties via the RF input pin.

23. (Original) The antenna according to Claim 19, wherein said at least one electronic component is a microchip configured send a challenge response in response to a challenge, said challenge response including a value related to said at least one property of the antenna.

24. (Original) The antenna according to Claim 19, wherein said at least one electronic component is located in a location that it cannot be easily removed or modified.

25. (Original) The antenna according to Claim 19, wherein said at least one electronic component is substantially surrounded by said at least one antenna element.

26. (Original) The antenna according to Claim 19, wherein said at least one electronic component is embedded within a substrate holding said at least one antenna element.

27. (Original) The antenna according to Claim 26, wherein said at least one electronic component is substantially surrounded by said at least one antenna element.

28. (Original) The antenna according to Claim 19, further comprising:

a ground pin;

wherein:

said at least one antenna element comprises a first antenna element connected to said RF input pin and a second antenna element connected to said ground pin; and

said at least one electronic component is connected between said RF pin and said ground pin.

29. (Original) The antenna according to Claim 28, further comprising:

a substrate having first and second surfaces, the first antenna element disposed on the first surface and the second antenna element is disposed on the second surface.

30. (Original) The antenna according to Claim 29, wherein said at least one electronic component is disposed between the first antenna element and the second antenna element and within said substrate.

31. (Original) The antenna according to Claim 28, wherein said antenna is a 5 GHz connectorized antenna.

32. (Original) The antenna according to Claim 19, wherein said antenna is a dual element planar antenna.

33-35. (Cancelled)

36. (Currently Amended) An ~~The~~ antenna ~~according to Claim 33~~ , comprising:

a set of data pins and an RF input pin;

at least one antenna element connected to the RF input pin; and

a series of shorts and opens connected to a set of data pins;

wherein said antenna is a dual element planar antenna.

37. (Original) An antenna, comprising:

a set of input pins and an RF input pin;

at least one antenna element connected to the RF input pin; and

at least one electronic component connected to the set of input pins;

wherein said at least one electronic component has a value related to at least one property of the antenna.

38. (Original) The antenna according to Claim 37, wherein said electronic component is a microchip configured to transmit at least a value related to at least one property of the antenna.

39. (Original) The antenna according to Claim 37, wherein said at least one electronic component is a circuit having a resonant frequency that identifies at least one property of the antenna.

40. (Original) The antenna according to Claim 37, wherein said at least one electronic component is a resistor having a resistance value that identifies at least one property of the antenna.

41. (Original) The antenna according to Claim 37, wherein said at least one electronic component is an active circuit powered from a source connected to one of the input pins.

42. (Original) The antenna according to Claim 37, wherein said antenna is a dual element planar antenna.

43-61. (Cancelled)

62. (Currently Amended) A ~~The~~ method according ~~to Claim 61~~, comprising the steps of:

preparing a substrate;

disposing at least one antenna element on the substrate;

attaching a connector to said at least one antenna element; and

inserting at least one electronic component on the substrate in a location where it is not easily removed or modified;

wherein said location is surrounded by said at least one antenna element.

63. (Cancelled)

64. (Currently Amended) A ~~The~~ method according ~~to Claim 61~~, comprising the steps of:

preparing a substrate;

disposing at least one antenna element on the substrate;

attaching a connector to said at least one antenna element; and

inserting at least one electronic component on the substrate in a location where it is not easily removed or modified;

wherein said electronic component is one of a resistor having a value selected to identify properties of the antenna, an resonant circuit having a resonant frequency that identifies properties of the antenna, and a microchip configured to transmit properties of the antenna.

65. (Currently Amended) A ~~The~~ method according ~~to Claim 61~~, comprising the steps of:

preparing a substrate;

disposing at least one antenna element on the substrate;

attaching a connector to said at least one antenna element; and

inserting at least one electronic component on the substrate in a location where it is not easily removed or modified;

wherein said antenna is a dual element planar antenna.

66. (New) A method of manufacturing an antenna, comprising the steps of:

disposing at least one antenna element on a substrate;

attaching a connector to said at least one antenna element; and

inserting at least one electronic component on the substrate;

wherein the electronic component has a value related to at least one property of the antenna.

67. (New) The method according to Claim 66, wherein said antenna is a dual element planar antenna.

68. (New) The method according to Claim 66, wherein the location is surrounded by said at least one antenna element.

69. (New) A method of manufacturing an antenna, comprising the steps of:

disposing at least one antenna element on a substrate;

attaching a connector to said at least one antenna element; and

inserting at least one electronic component on the substrate;

wherein the electronic component has a value related to at least one property of the antenna.

70. (New) The method according to Claim 69, wherein the location is embedded in the substrate.

71. (New) The method according to Claim 69, wherein:

the antenna comprises, a set of data pins and an RF input pin, at least one antenna element connected to the RF input pin, and a series of shorts and opens connected to a set of data pins.

72. (New) The method according to Claim 76, wherein said shorts comprise grounded pins and said opens comprise pins which are not grounded.

73. (New) The method according to Claim 71, wherein said shorts comprise grounded pins and said opens comprise pins connected to a voltage source.